



IN THE UNITED STATES  
PATENT AND TRADEMARK OFFICE

**PATENT APPLICATION**

Applicant(s): **Cabral, et al.**

Case: **YOR920030469US1**

Serial No.: 10/669,898

Filed: **September 24, 2003**

Examiner:

Group Art Unit: 2812

Title: **METHOD AND APPARATUS FOR FABRICATING CMOS FIELD  
EFFECT TRANSISTORS**

MAIL STOP Missing Parts  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

S I R:

**PRELIMINARY AMENDMENT**

Prior to examination, please amend the above-identified application as follows:

**IN THE DRAWINGS**

Please replace Sheet 3 with the erroneous label of "Fig 1" with Sheet 3 with the proper label of "Fig 3".

Attachment: Replacement Sheet  
Annotated Sheet Showing Changes